Layer Stack Up Detail for: TIDA-00490

Layer Name	Gerber Document	Copper Thickness	Dielectric Material
Top Solder Mask	(.GTS)		Solder Resist
Top Layer	(.GTL)	1.4mil	FR4
Bottom Layer	(.GBL)	1.4mil	
Bottom Solder Mask	(.GBS)		Solder Resist

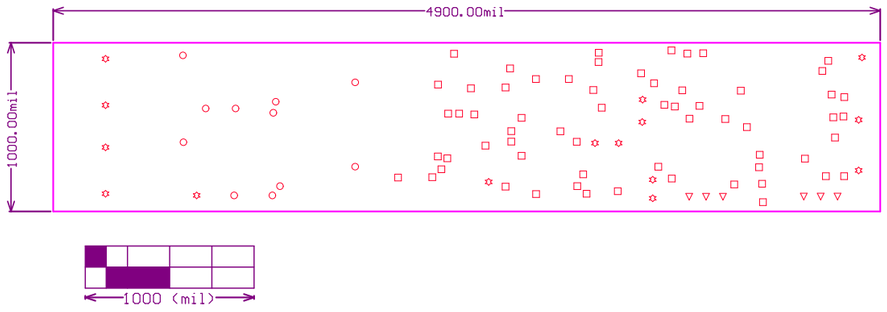
DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)
4900MIL X 1000MIL

Number of Layers : 2
MIN. TRACK WIDTH: 8 MIL
MIN. CLEARANCE: 8 MIL
MIN. VIA PAD SIZE: 30 MIL

MINIMUM ANNULAR RING 0.177mm (7MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
[ ] FR-408 [ ] FR-4 High Tg [X] OTHER FR4
THICKNESS: [X] 63 MIL (1.6mm) +/-10% [ ] OTHER
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
COPPER THICKNESS (FINISHED):
OUTER: [X] 1.4MIL (1oz) [ ] 2MIL (1.4oz) [ ] 2.8MIL (2oz)
INNER SIGNAL: [ ] 1.4MIL (1oz) [ ] 2.8MIL (2oz) [ ] N/A
DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER
BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER
SOLDER RESIST COLOR:
[X] GREEN [ ] BLUE [ ] OTHER
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] ENEPIG
[ ] IMM. TIN/SILVER OR EQUIV [ ] OTHER
ARRAY/PANEL: [ ] CUT AND TRIM PER MECH LAYER 1
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] UL 94V-0 [X] RoHS [ ] OTHER PER ORDER
ADDITIONAL REQUIREMENTS: VIA TENTING: YES [X] NO [ ]
MICROSECTION: [ ] YES IMPEDANCE CONTROL: YES [ ] NO [X]
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER
MANUFACTURER'S UL: [ ] RAIL [ ] METAL [X] SILK



Symbol	Hit Count	Tool Size	Plated	Hole Type
□	62	16mil (0.406mm)	PTH	Round
○	11	20mil (0.508mm)	PTH	Round
▽	6	33.465mil (0.85mm)	PTH	Round
☼	15	40mil (1.016mm)	PTH	Round
	94 Total			

Drill Table
FOR 16MIL DRILL +0/-16MIL
FOR 20MIL DRILL +0/-20MIL
FOR PTH DRILL +/-3MIL

TEXAS INSTRUMENTS

PROJECT TITLE: AC/DC BINARY INPUT
DESIGNED FOR: Public Release
FILE NAME: TIDA-00490_AC_Binary.PcbDoc

ENGINEER:	LAYOUT BY:
Sreenivas	Manjunath M
SCALE: 1.09	ALTIUM DESIGNER VERSION: 10.0.0.27009

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00490	REV: E2	SVN REV: Not In VersionControl
LAYER NAME = Drill Drawing			
PLOT NAME = Fabrication Drawing	GENERATED : 3/13/2015 7:30:52 PM		TEXAS INSTRUMENTS

Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.